

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

## U.S. UTILITY Patent Application

<p>O.I.P.E.</p> <p>FA</p> <p>SCANNED <u>17</u> Q.A. <u>Am</u></p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/862689	CONT/PRIOR	CLASS <del>361</del> 379	SUBCLASS 349.01	ART UNIT 284-1 2643	EXAMINER JAMAL STICK
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## APPLICANTS

Thomas Beutler  
Raphael Rahamim

**TITLE**

Circuit board capacitor structure for forming a high voltage isolation barrier

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____			<b>ISSUE FEE</b>	
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<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	
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